

#### Brief File Descriptions:

- reportSummer.xcos
  - The Summing Amplifier discussed in the final report.
- reportVCA.xcos
  - The Voltage Controlled Amplifier discussed in the final report.
- reportVCO.xcos
  - The Voltage Controlled Oscillator discussed in the final report.
- CSI\_MBLIF.xcos
  - The CSI Macro-Blif used to create the synthesizer.
- Final\_Design.xcos
  - Final Compiled Design implementing all blocks.
- NMOS\_Mirror.xcos
- PMOS\_Mirror.xcos
- Small\_Oscillator.xcos
- Summing\_Amplifier.xcos
- VCA\_Re-organized.xcos
  - VCA design reorganized to look better. Same functionality as VCA\_Test1.xcos
- VCA\_Test1.xcos
  - Test of the VCA alone.
- VCO\_Plus\_Summing\_Amplifier\_Test.xcos
  - Test of the VCO connected to a summing amplifier.
- VCO3.xcos
- Voltage\_Summer.xcos
- Working\_VCO.xcos